PATENT NUMBER and ISSUE DATE

U.S. **UTILITY** Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU		EXAMINER			
10051701	01/17/2002	349	47	2871	Duch	J. TAIL	THOi		
**APPLICANT	S: Jeon Jir	n;							
**CONTINUING DATA VERIFIED:									
** FOREIGN APPLICATIONS VERIFIED:									
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Foreign priority c		<u>ا</u> پ ت	es 🗅 no	IIVO	ATTOR	NEY DOCKE	TNO		
35 USC 119 conditions met ☐ yes ☐ no					8054-4 (LW7020US)				
TITLE. This film transistor substrate for liquid crystal display panel and manufacturing method thereof									
USDEPT OF COMMUNAL S.TM.PTO 436L(Rev. 12.95)									

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED					
		Assistant Examiner	Total Claims		Print Claim for O.G		
ISSUE FEE			DRAWING				
Amount Due	Date Paid	7	Sheets Drwg.	Figs.Drwg.	Print Fig.		
		Primary Examiner		<u></u>	<u> </u>		
TERMINAL DISCLAMER		PREPARED FOR ISSUE	Application Examiner				
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			(CRF)		CD-ROM		